



TEST DATA OF MUS154812

Regulated DC Power Supply
July 1, 2026

Approved by : Masahiro Shima
Design Manager

Prepared by : Kosei Oba
Design Engineer

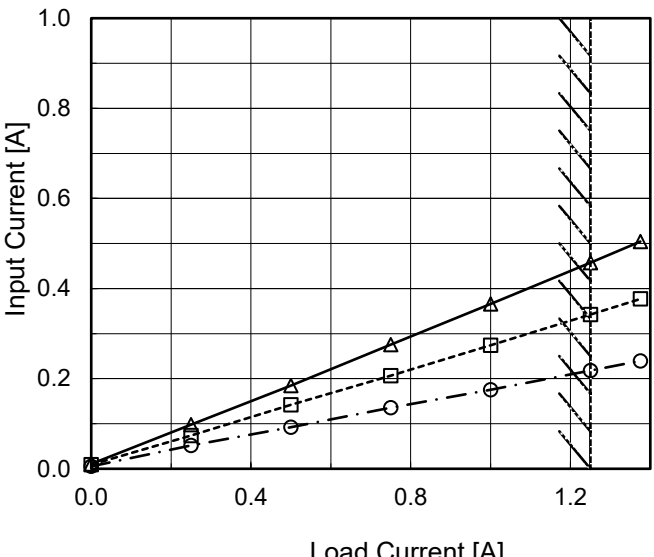
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Model		MUS154812	Temperature		25°C																																																			
Item		Input Current (by Load Current)	Testing Circuitry		Figure A																																																			
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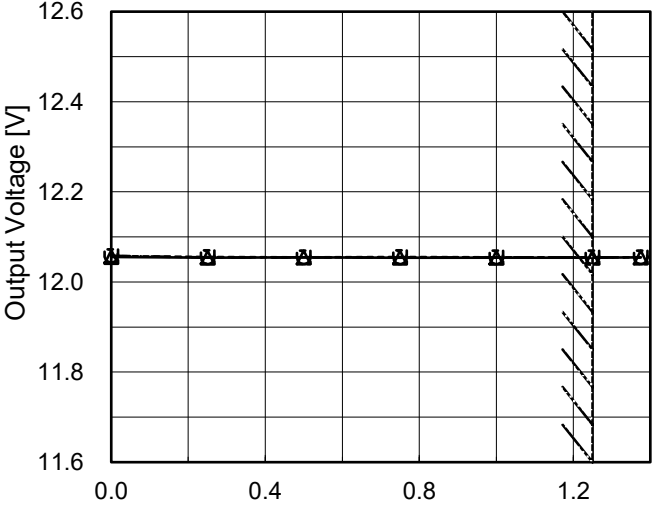
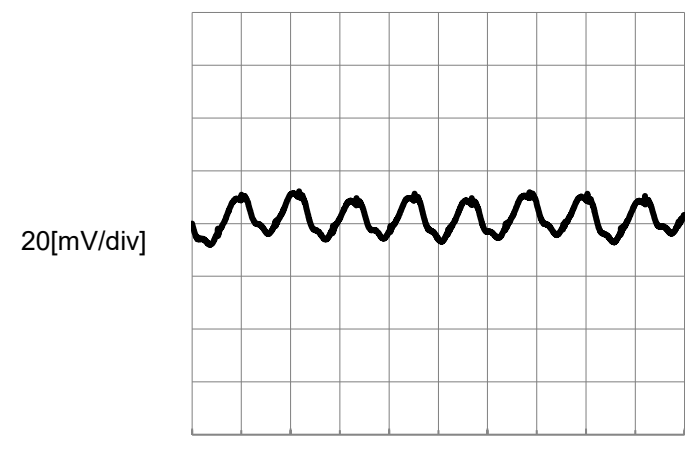
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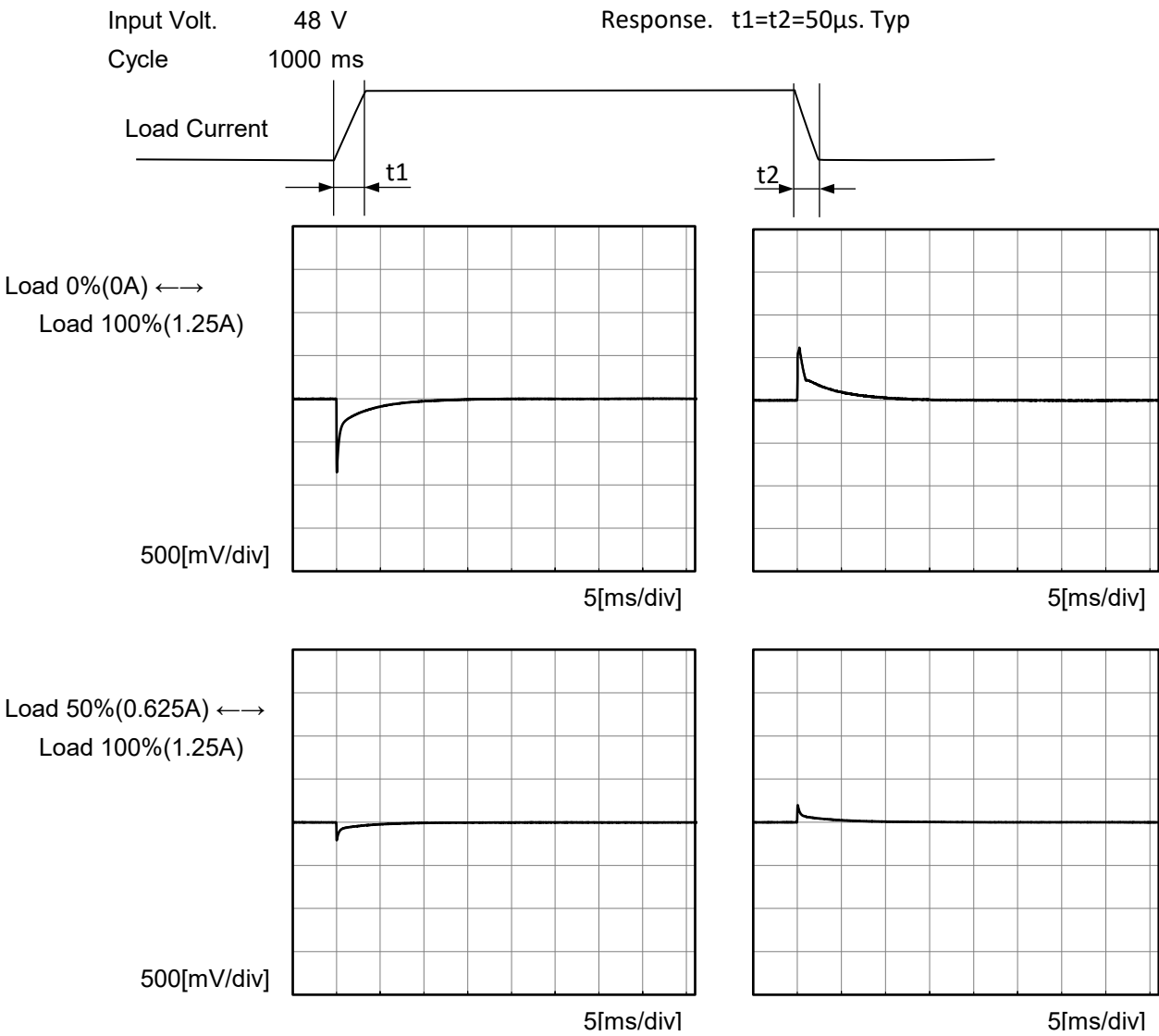
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Item	Line Regulation																																		
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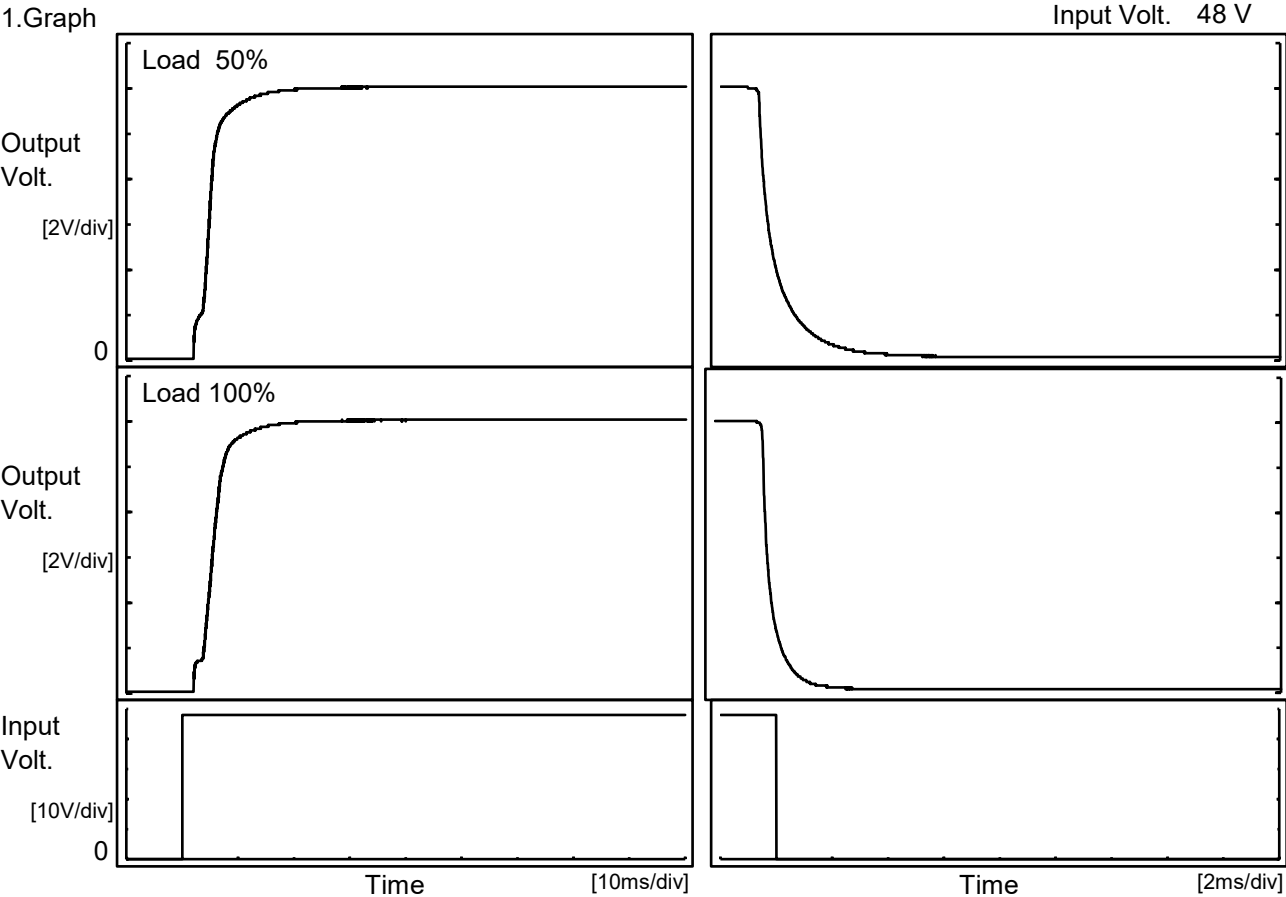
Model		MUS154812	Temperature 25°C Testing Circuitry Figure A
Item		Dynamic Load Response	
Object		+12V1.25A	





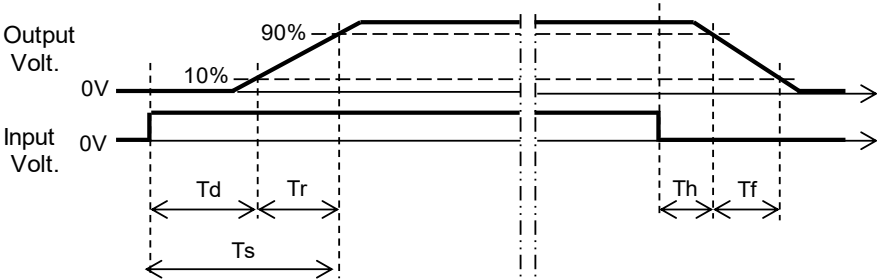
Model		MUS154812	Temperature 25°C Testing Circuitry Figure A
Item		Rise and Fall Time	
Object		+12V1.25A	

1.Graph



2.Values

		[ms]				
Load	Time	Td	Tr	Ts	Th	Tf
50 %		2.2	5.7	7.9	0.6	1.8
100 %		2.4	5.9	8.3	0.3	0.9



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		Testing Circuitry Figure A
Model	MUS154812	
Item	Ambient Temperature Drift	
Object	+12V1.25A	

1.Values

Load 100%

Ambient Temperature[°C]	Output Voltage [V]		
	Input Volt. 36V	Input Volt. 48V	Input Volt. 76V
-40	11.980	11.980	11.979
25	12.054	12.055	12.054
85	12.061	12.061	12.061

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A
Object	+12V1.25A	

1.Values

Ambient Temperature[°C]	Input Voltage [V]	
	Load 50%	Load 100%
-40	28.1	28.1
25	28.3	28.3
85	28.5	28.5

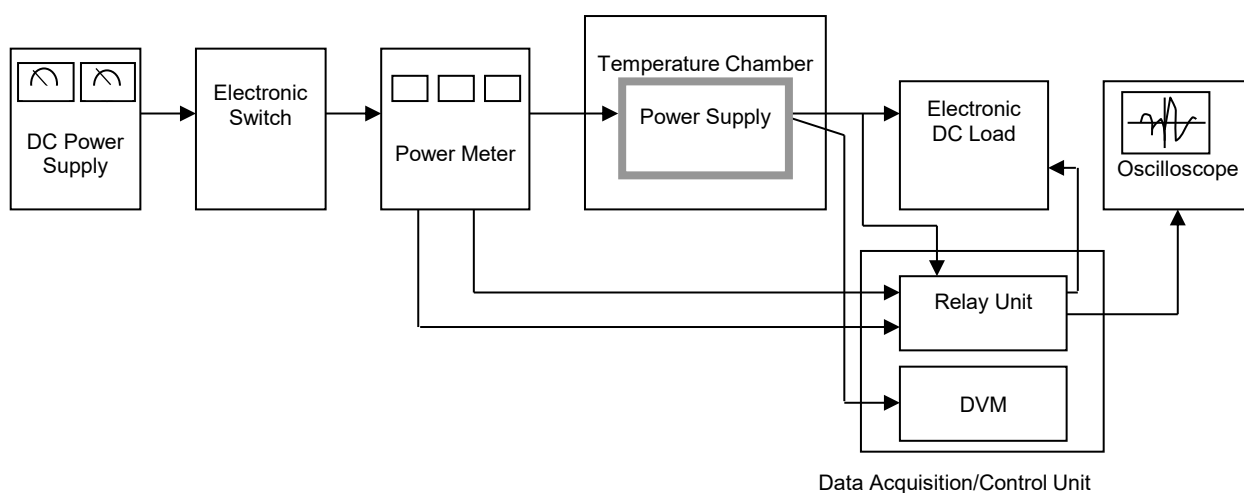


Figure A

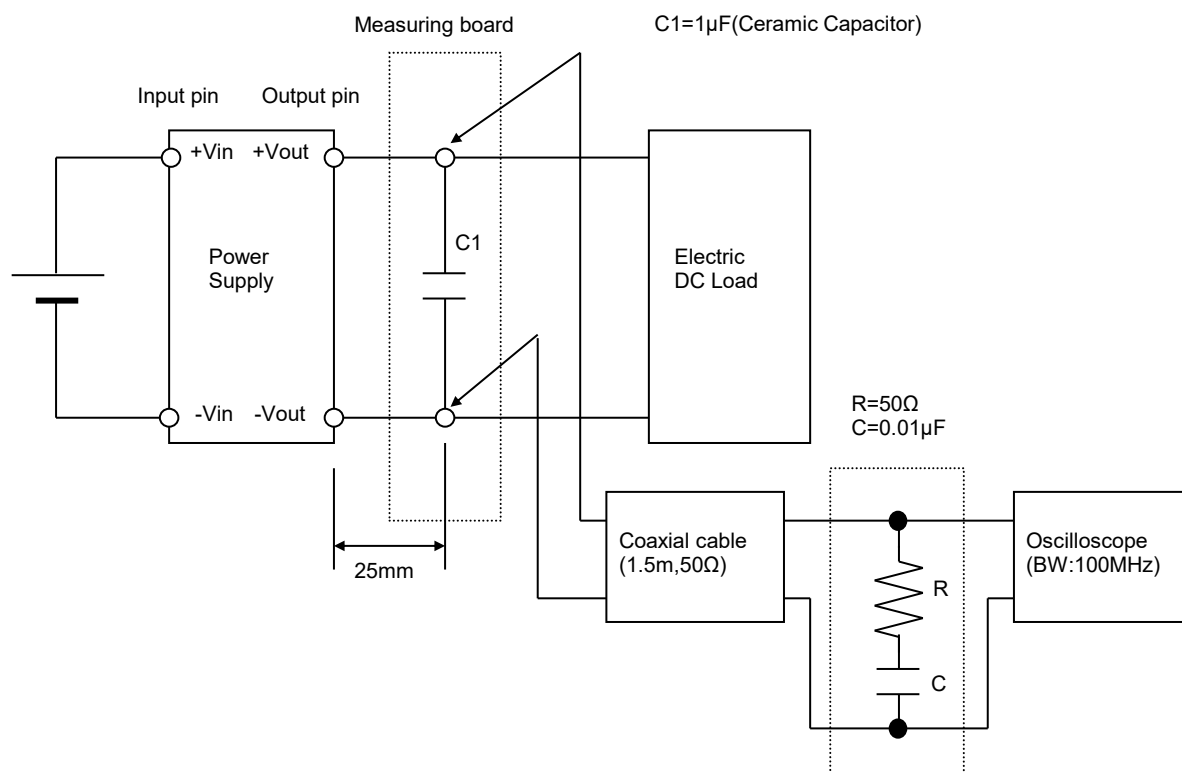


Figure B