For Reference

TEST DATA OF PDA75F-24

Regulated DC Power Supply
October 26, 2026

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COSEL CO.,LTD.

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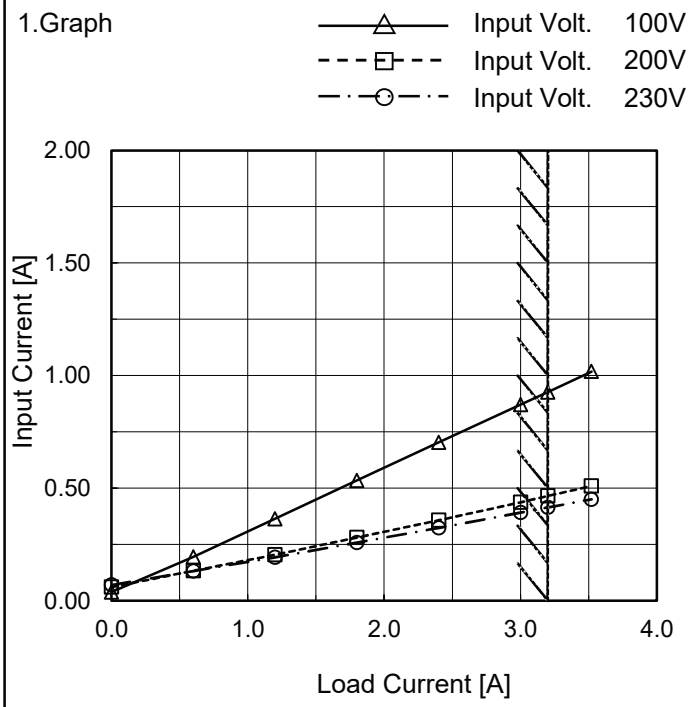
Model	PDA75F-24
Item	Input Current (by Load Current)
Object	———————

Temperature 25°C
Testing Circuitry Figure A

1.Graph



Note: Slanted line shows the range of the rated load current.

2.Values

Load Current [A]	Input Current [A]		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
0.00	0.039	0.061	0.070
0.60	0.194	0.133	0.132
1.20	0.364	0.205	0.193
1.80	0.534	0.281	0.258
2.40	0.703	0.358	0.324
3.00	0.871	0.438	0.391
3.20	0.927	0.466	0.414
3.52	1.018	0.510	0.451
--	-	-	-
--	-	-	-
--	-	-	-

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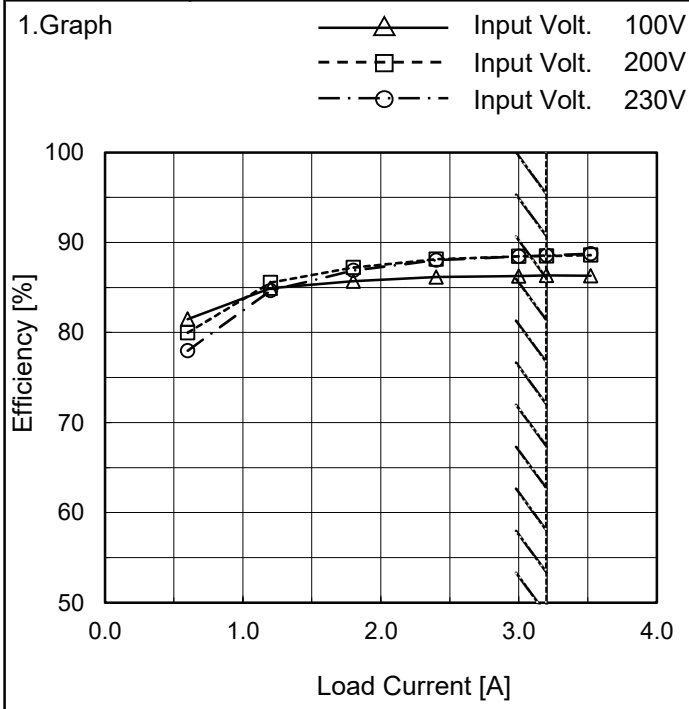
Model	PDA75F-24
Item	Efficiency (by Load Current)
Object	———————

Temperature 25°C
Testing Circuitry Figure A

1.Graph



Note: Slanted line shows the range of the rated load current.

2.Values

Load Current [A]	Efficiency [%]		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
0.00	-	-	-
0.60	81.5	80.0	78.0
1.20	84.9	85.6	84.7
1.80	85.7	87.2	86.9
2.40	86.2	88.2	88.0
3.00	86.3	88.5	88.5
3.20	86.3	88.5	88.6
3.52	86.3	88.6	88.8
--	-	-	-
--	-	-	-
--	-	-	-

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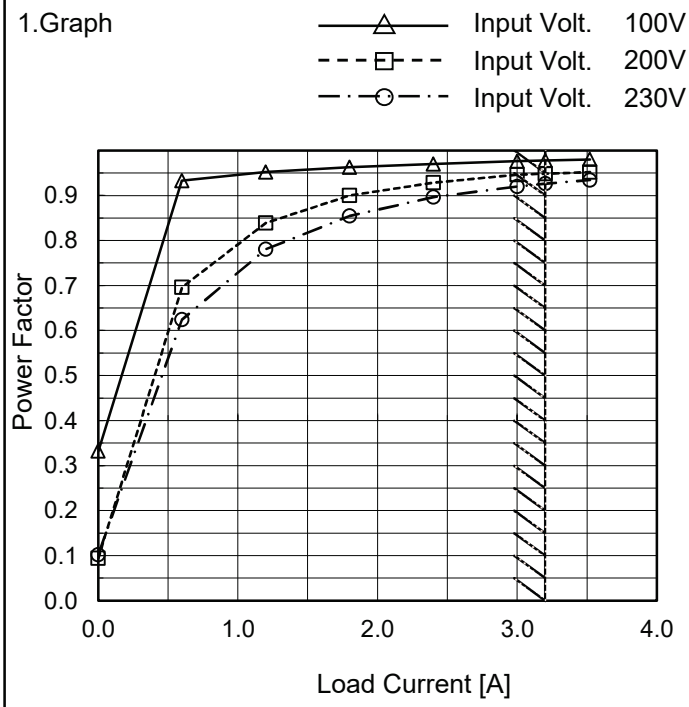
Model	PDA75F-24
Item	Power Factor (by Load Current)
Object	———————

Temperature 25°C
Testing Circuitry Figure A

1.Graph



Note: Slanted line shows the range of the rated load current.

2.Values

Load Current [A]	Power Factor		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
0.00	0.332	0.094	0.102
0.60	0.933	0.696	0.624
1.20	0.952	0.839	0.781
1.80	0.963	0.900	0.855
2.40	0.970	0.929	0.897
3.00	0.976	0.946	0.920
3.20	0.977	0.948	0.927
3.52	0.980	0.952	0.934
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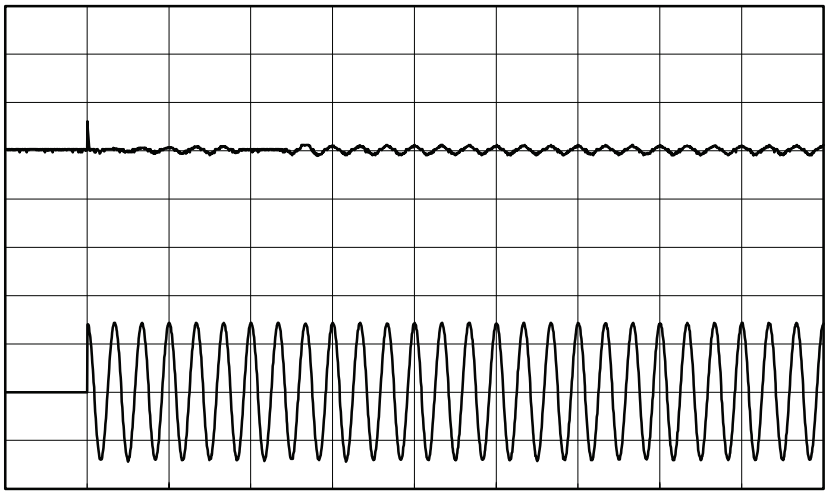
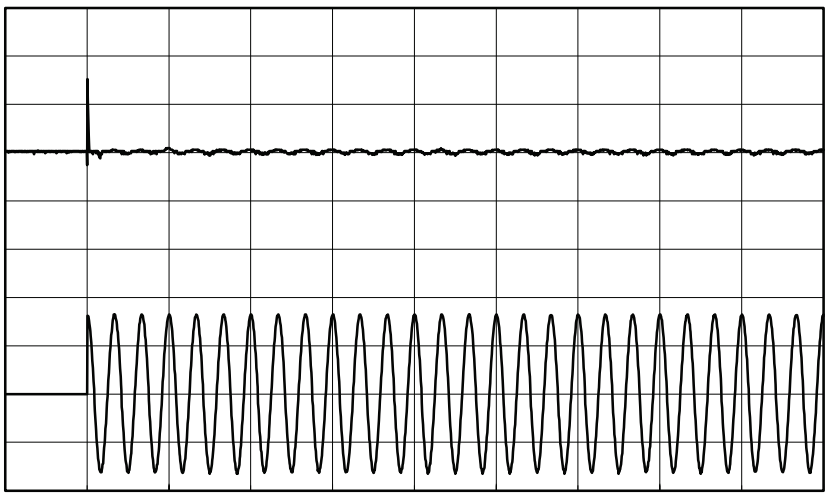
Model	PDA75F-24
Item	Inrush Current
Object	————————

Temperature 25°C
Testing Circuitry Figure A



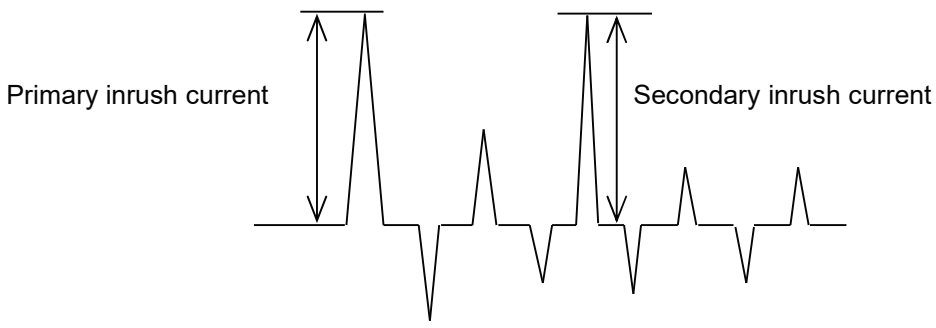
Input Voltage 100 V
Frequency 60 Hz
Load 100 %

Primary inrush current :
12.3 A
Secondary inrush current :
2.4 A



Input Voltage 230 V
Frequency 60 Hz
Load 100 %

Primary inrush current :
30.6 A
Secondary inrush current :
1.8 A



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Model	PDA75F-24	
Item	Leakage Current	Temperature 25°C Testing Circuitry Figure C
Object	———————————	

1.Results

[mA]

Standards	Testing Circuitry	Measuring Method	Input Volt.			Note
			100 [V]	230 [V]	240 [V]	
DEN-AN	Figure C-1	Both phases	0.13	0.33	0.35	Operation
		One of phases	0.25	0.63	0.66	Stand by
IEC62368-1	Figure C-2	Both phases	0.13	0.33	0.34	Operation
		One of phases	0.25	0.63	0.65	Stand by
	Figure C-3	Both phases	0.13	0.32	0.34	Operation
		One of phases	0.25	0.62	0.65	Stand by

The value for "One of phases" is the reference value only.

2.Condition

Leakage current value is concluded after measuring both phases of AC input and by choosing the larger one.

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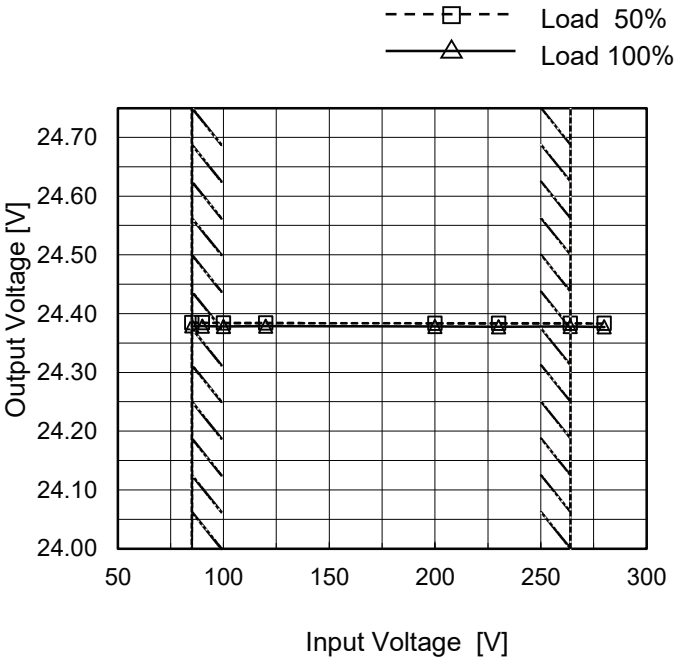
Model	PDA75F-24
Item	Line Regulation
Object	+24V3.2A

Temperature 25°C
Testing Circuitry Figure A

1.Graph



Note: Slanted line shows the range of the rated input voltage.

2.Values

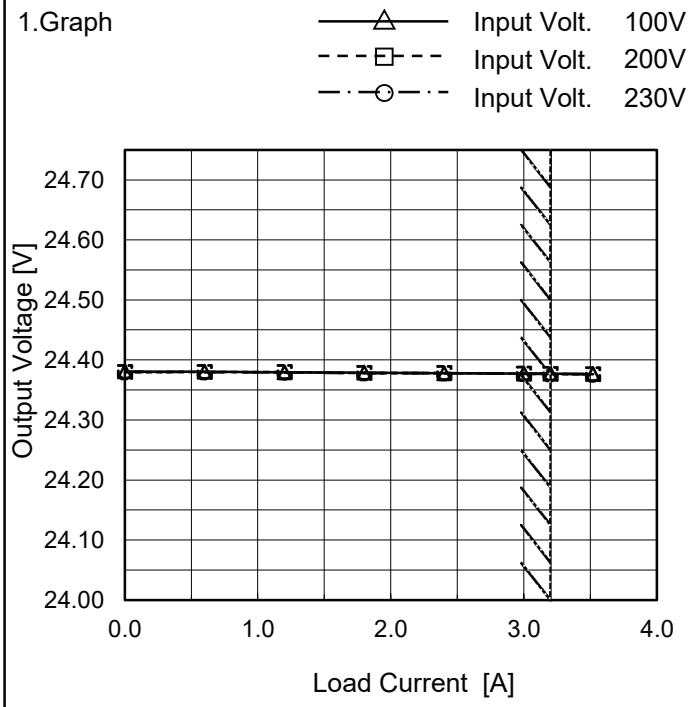
Input Voltage [V]	Output Voltage [V]	
	Load 50%	Load 100%
85	24.385	24.379
90	24.385	24.379
100	24.384	24.378
120	24.384	24.379
200	24.384	24.378
230	24.384	24.378
264	24.384	24.378
280	24.383	24.378
--	-	-

COSEL

Model	PDA75F-24
Item	Load Regulation
Object	+24V3.2A

Temperature 25°C
Testing Circuitry Figure A

1.Graph



Note: Slanted line shows the range of the rated load current.

2.Values

Load Current [A]	Output Voltage [V]		
	Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
0.00	24.381	24.380	24.381
0.60	24.380	24.380	24.380
1.20	24.379	24.380	24.379
1.80	24.379	24.379	24.379
2.40	24.378	24.378	24.378
3.00	24.377	24.377	24.377
3.20	24.377	24.377	24.377
3.52	24.377	24.376	24.376
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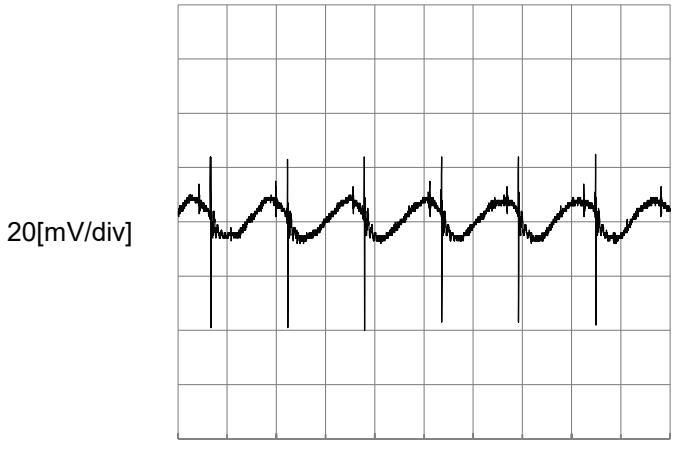
Item	Ripple-Noise
Object	+24V3.2A

Temperature 25°C
Testing Circuitry Figure B

1.Graph

Input Voltage 230V
Load 100%



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Model	PDA75F-24
Item	Dynamic Load Response
Object	+24V3.2A

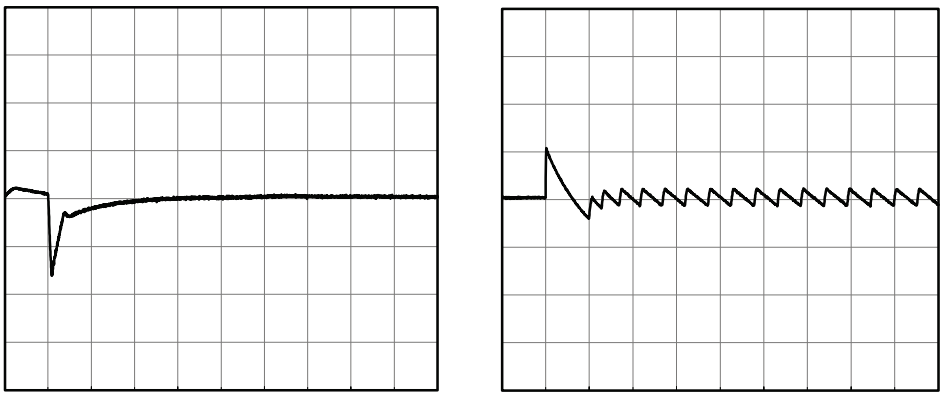
Temperature 25°C
Testing Circuitry Figure A

Input Volt. 230 V
Cycle 1000 ms

Response. t1=t2=50µs. Typ



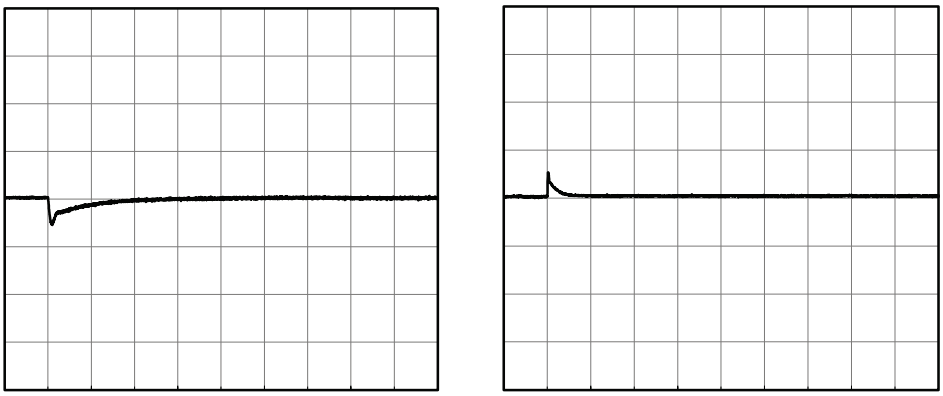
Load 0%(0A) ⟷ Load 100%(3.2A)



200[mV/div]

2[ms/div] 10[ms/div]

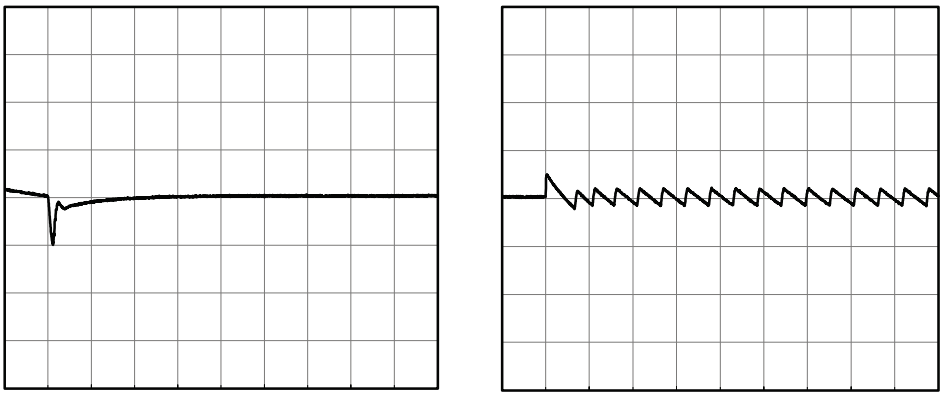
Load 50%(1.6A) ⟷ Load 100%(3.2A)



200[mV/div]

2[ms/div] 10[ms/div]

Load 0%(0A) ⟷ Load 50%(1.6A)



200[mV/div]

2[ms/div] 10[ms/div]

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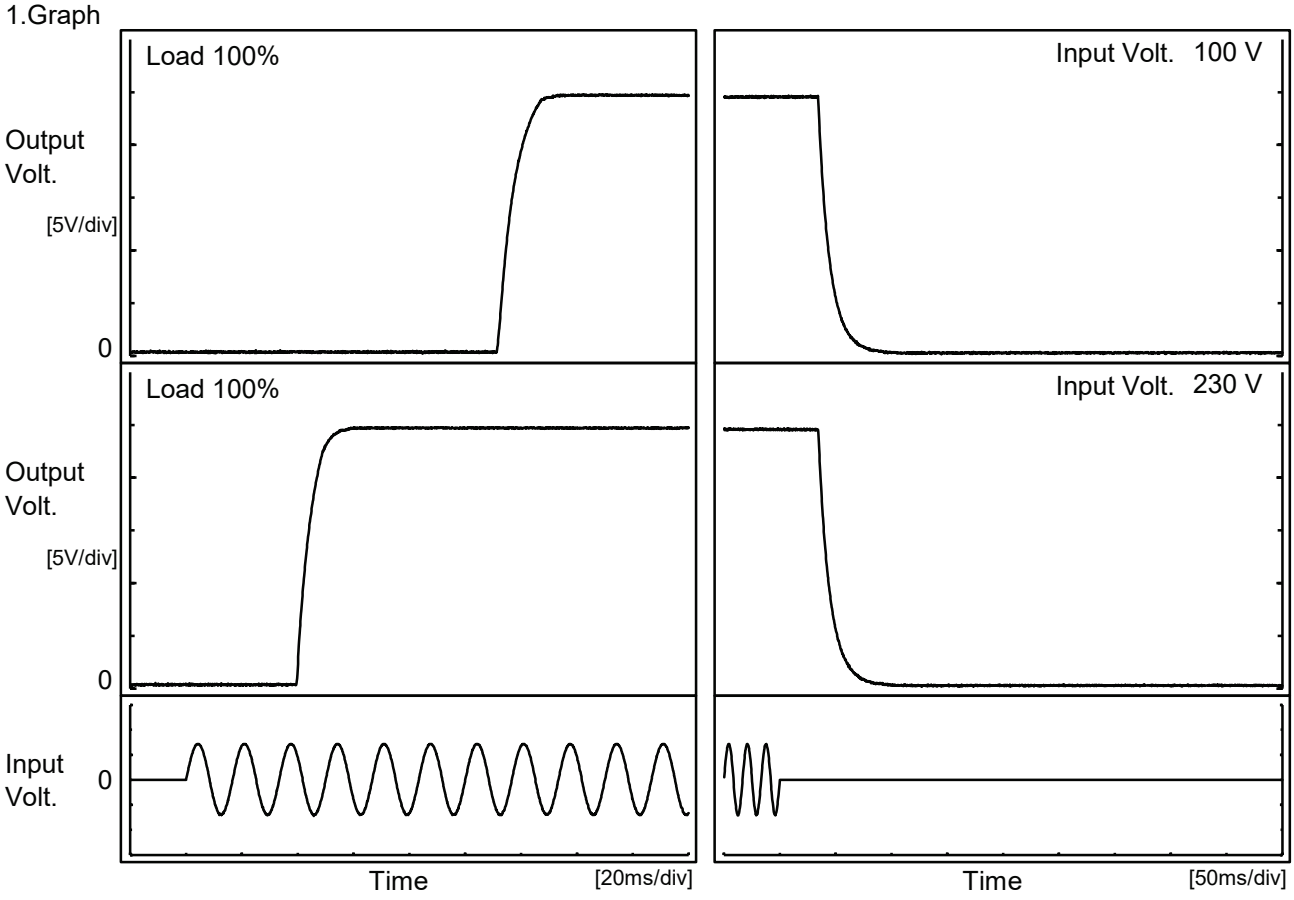
Model	PDA75F-24
Item	Rise and Fall Time
Object	+24V3.2A

Temperature 25°C
Testing Circuitry Figure A

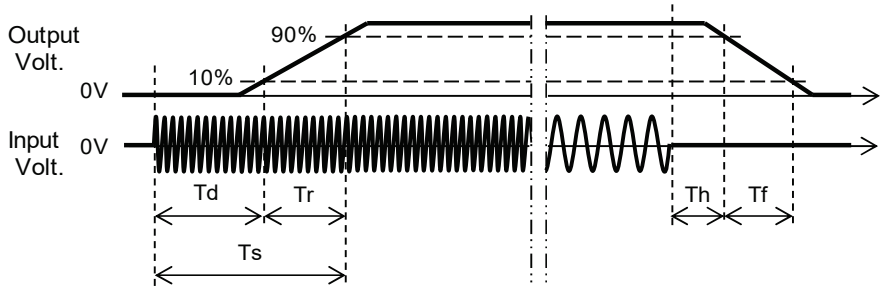
1.Graph



2.Values

[ms]

Input Volt. \ Time	Td	Tr	Ts	Th	Tf
100 V	112.3	10.4	122.7	35.3	23.3
230 V	40.2	8.2	48.4	35.5	23.8



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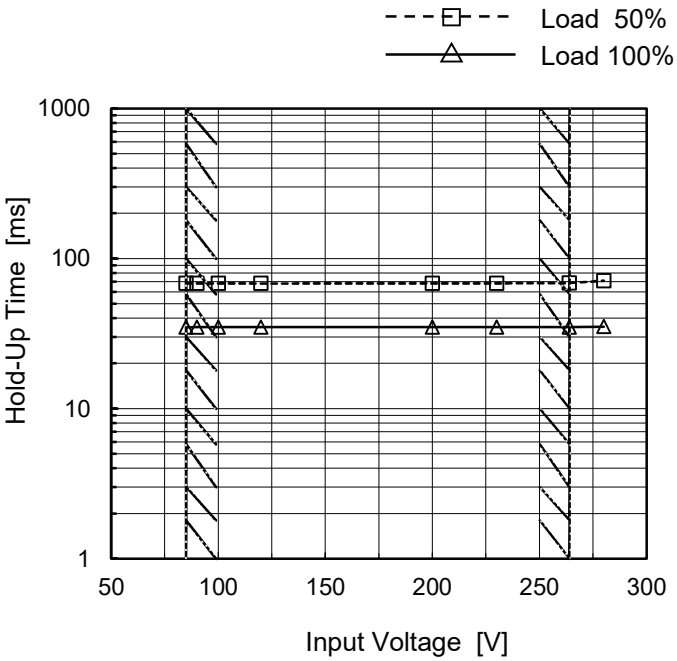
COSEL

Model	PDA75F-24
Item	Hold-Up Time
Object	+24V3.2A

Temperature 25°C
Testing Circuitry Figure A

1.Graph



This duration covers from Shut-off of input voltage to the moment when output voltage descends to the rated range of voltage accuracy.
Note: Slanted line shows the range of the rated input voltage.

2.Values

Input Voltage [V]	Hold-Up Time [ms]	
	Load 50%	Load 100%
85	68	35
90	68	35
100	68	35
120	68	35
200	68	35
230	68	35
264	69	35
280	71	35
--	-	-

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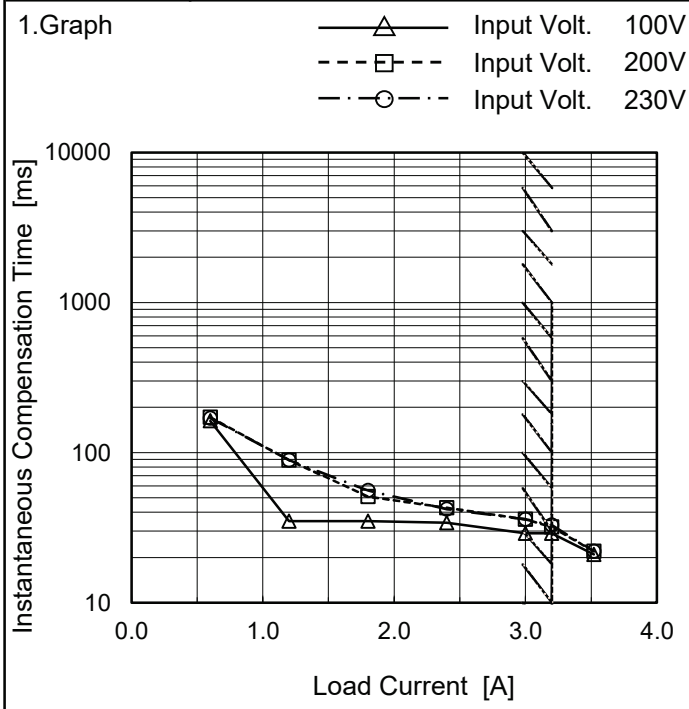
Model	PDA75F-24
Item	Instantaneous Interruption Compensation
Object	+24V3.2A

Temperature 25°C
Testing Circuitry Figure A

1.Graph



Note: Slanted line shows the range of the rated load current.

2.Values

Load Current [A]	Time [ms] Input Volt. 100[V]	Input Volt. 200[V]	Input Volt. 230[V]
0.00	-	-	-
0.60	163	172	170
1.20	35	89	89
1.80	35	51	56
2.40	34	43	42
3.00	29	36	36
3.20	29	32	33
3.52	21	22	22
--	-	-	-
--	-	-	-
--	-	-	-

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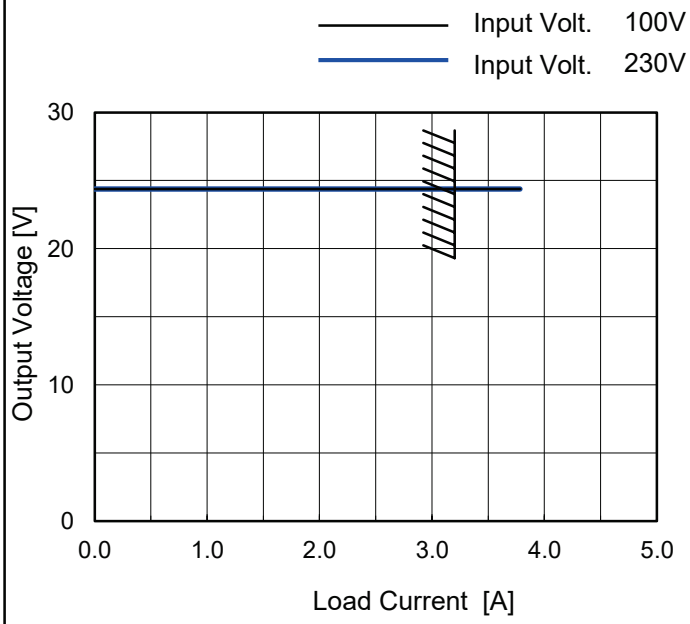
Model	PDA75F-24
Item	Overcurrent Protection
Object	+24V3.2A

Temperature 25°C
Testing Circuitry Figure A

1.Graph



Note: Slanted line shows the range of the rated load current.

Overcurrent protection is Hiccup mode.

2.Values

Output Voltage [V]	Load Current [A]	
	Input Volt. 100[V]	Input Volt. 230[V]
24.0	3.78	3.78
22.8	-	-
21.6	-	-
19.2	-	-
16.8	-	-
14.4	-	-
12.0	-	-
9.6	-	-
7.2	-	-
4.8	-	-
2.4	-	-
0.0	-	-

COSEL

Model	PDA75F-24	
Item	Ambient Temperature Drift	Testing Circuitry Figure A
Object	+24V3.2A	

1.Values Load 100%

Ambient Temperature[℃]	Output Voltage [V]		
	Input Volt. 100V	Input Volt. 200V	Input Volt. 230V
-20	24.424	24.424	24.424
25	24.382	24.382	24.381
50	24.331	24.331	24.331

Item	Minimum Input Voltage for Regulated Output Voltage	Testing Circuitry Figure A
Object	+24V3.2A	

1.Values

Ambient Temperature[℃]	Input Voltage [V]	
	Load 50%	Load 100%
-20	41	59
25	41	55
50	41	55

Item	Overvoltage Protection	Testing Circuitry Figure A
Object	+24V3.2A	

1.Values Load 0%

Ambient Temperature[℃]	Operating Point [V]	
	Input Volt. 100V	Input Volt. 230V
-20	32.21	32.14
25	33.26	33.19
50	33.83	33.76

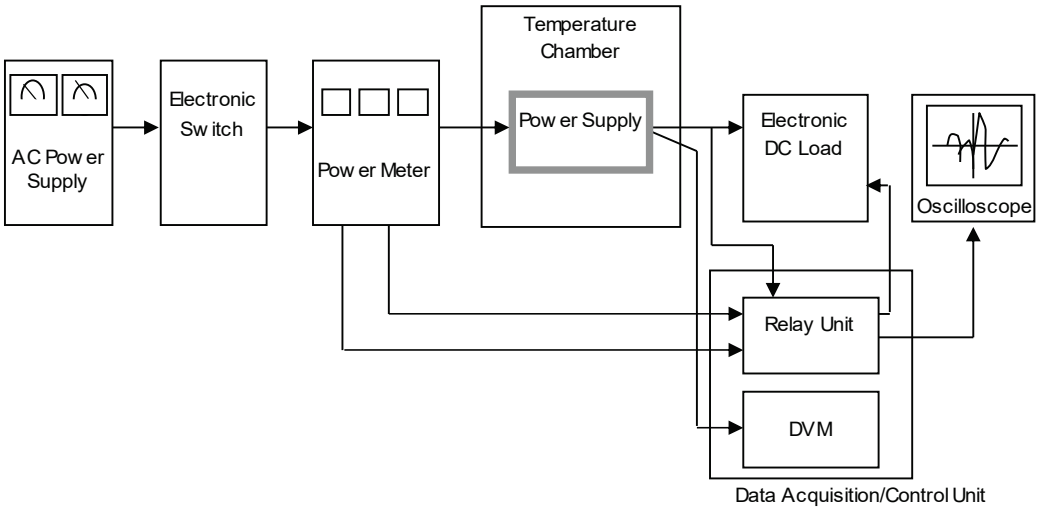
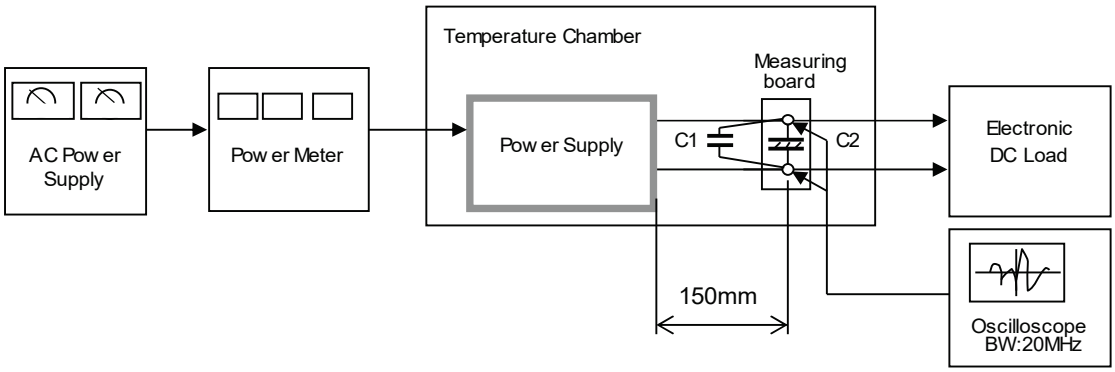


Figure A



C1= 0.1 μF
(Ceramic capacitor)

C2= 22 μF
(Electrolytic capacitor)

Figure B

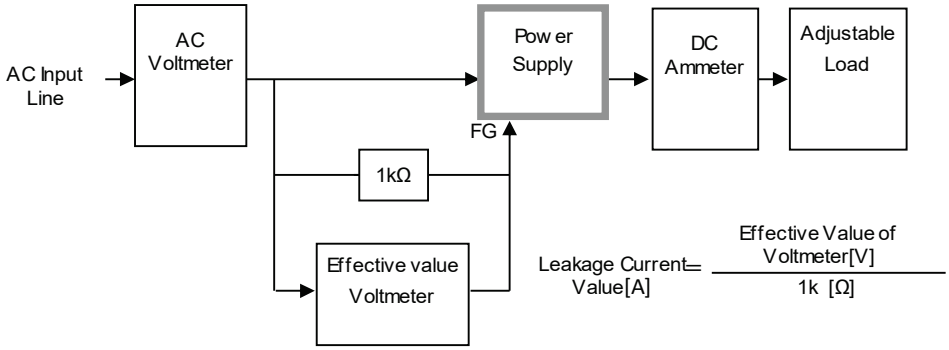


Figure C-1 (DEN-AN)

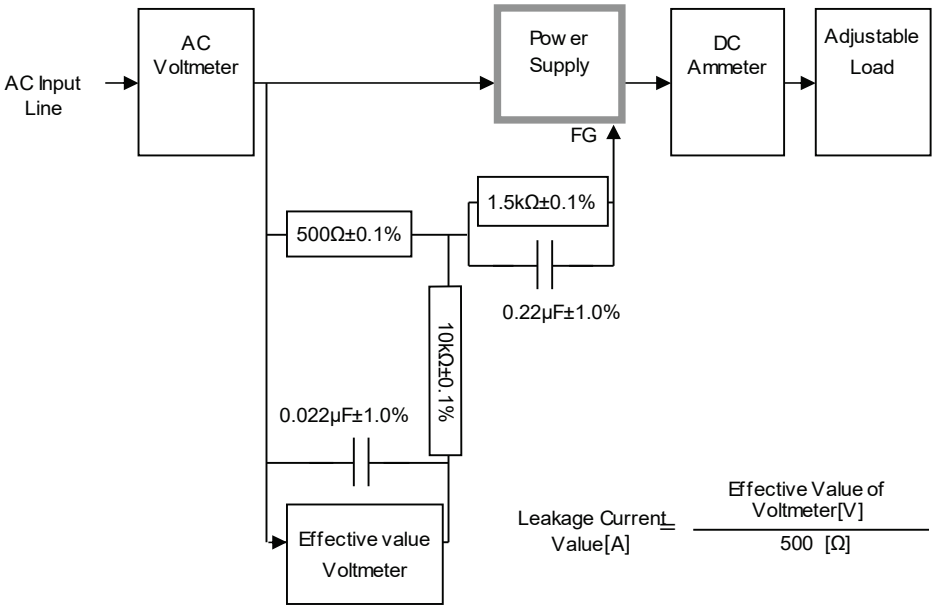


Figure C-2 (IEC62368-1 refer to IEC60990 Fig.4)

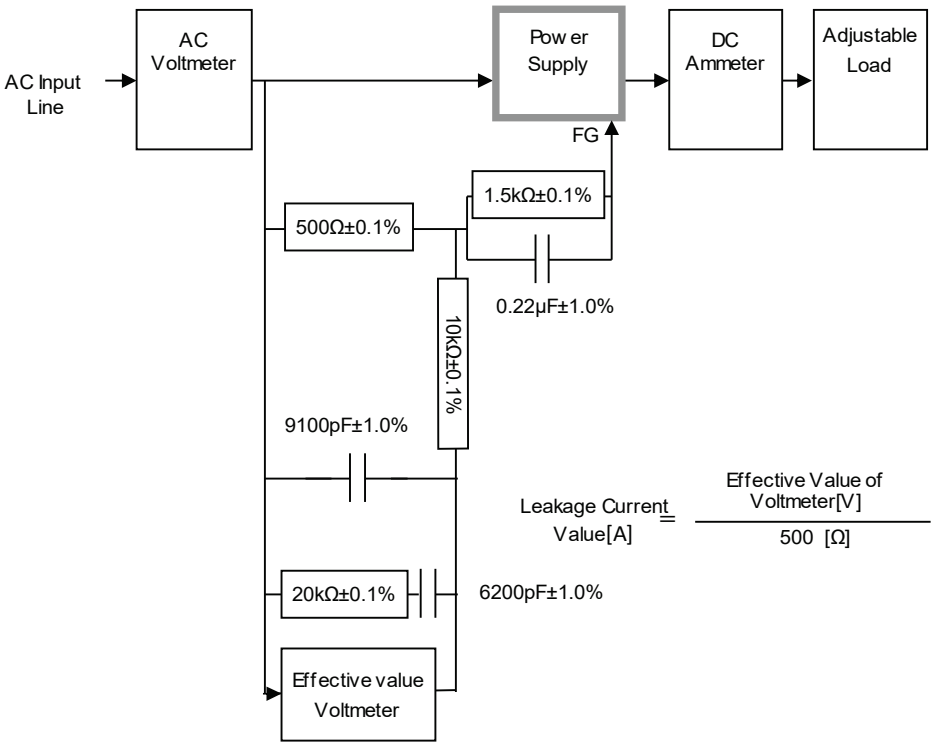


Figure C-3 (IEC62368-1 refer to IEC60990 Fig.5)